

Fig. 1
PRIOR ART

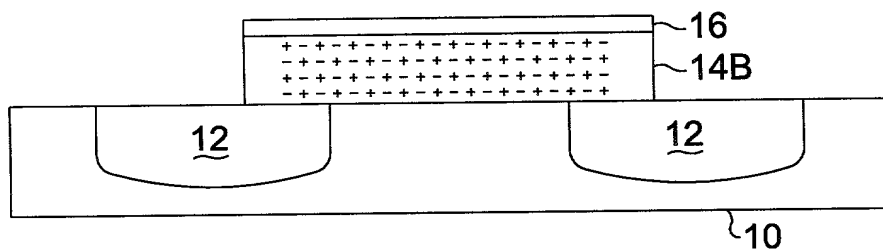


Fig. 2
PRIOR ART

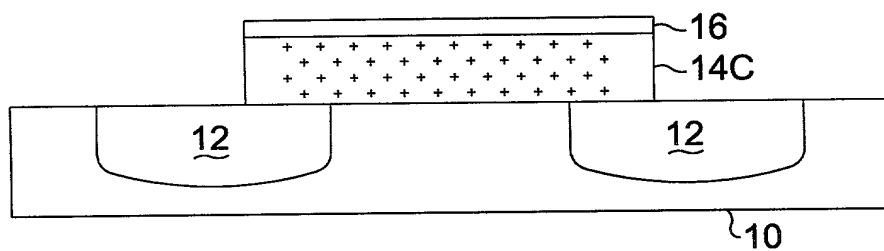


Fig. 3
PRIOR ART

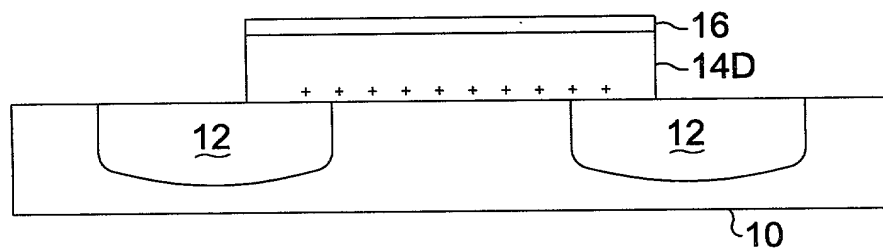
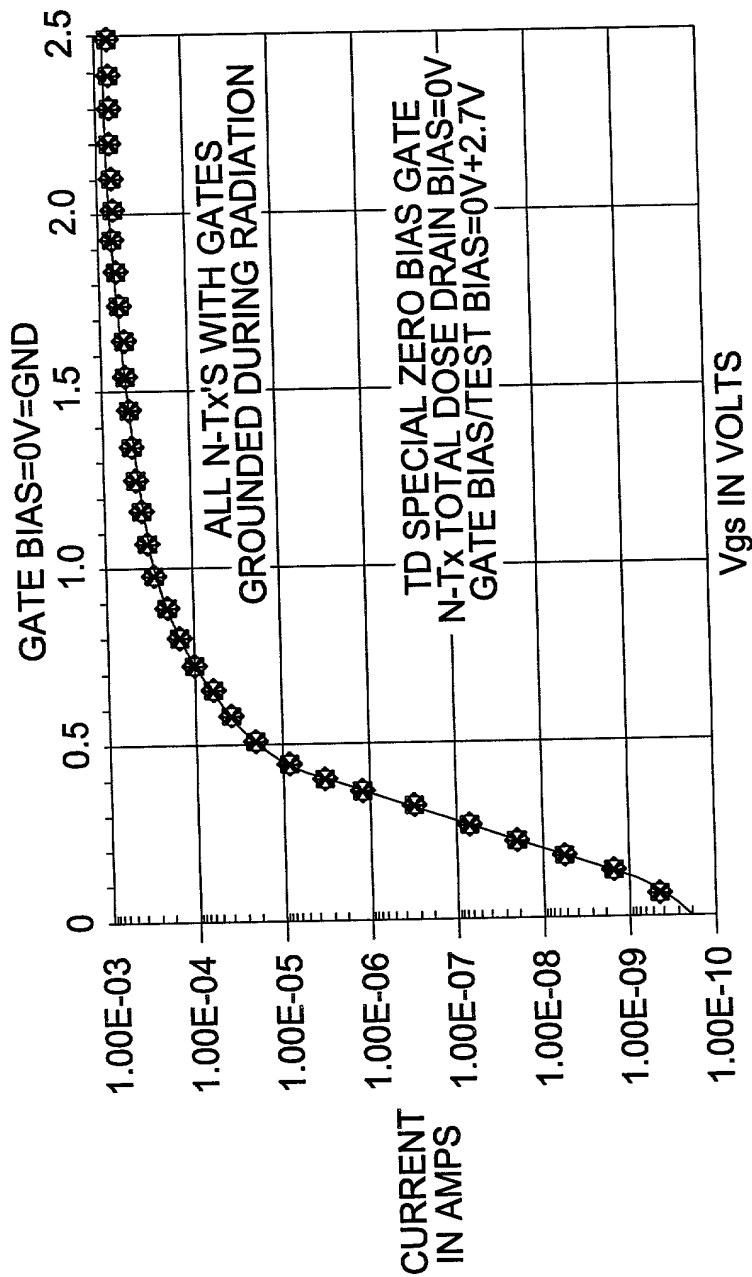


Fig. 4
PRIOR ART

NORMAL TRANSISTOR SATURATED CURRENT vs.
TOTAL DOSE-ZERO GATE BIAS



Vd=0V TD BIAS (STD DURING TOTAL DOSE)
Vd=+2.7V CURRENT MEAS.

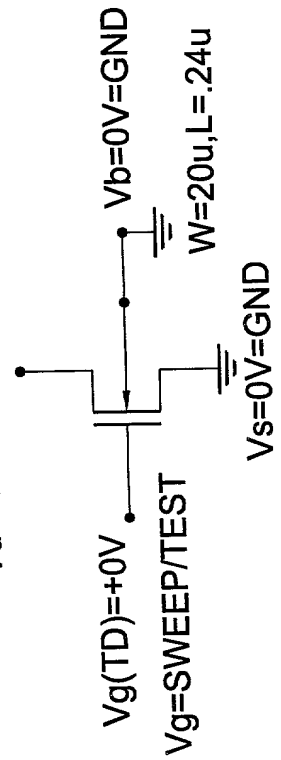
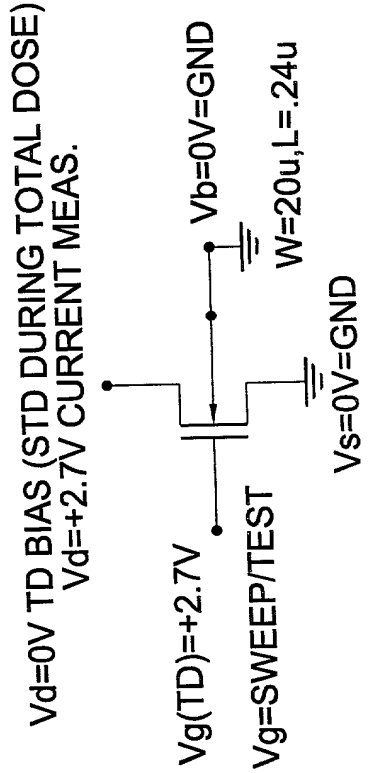
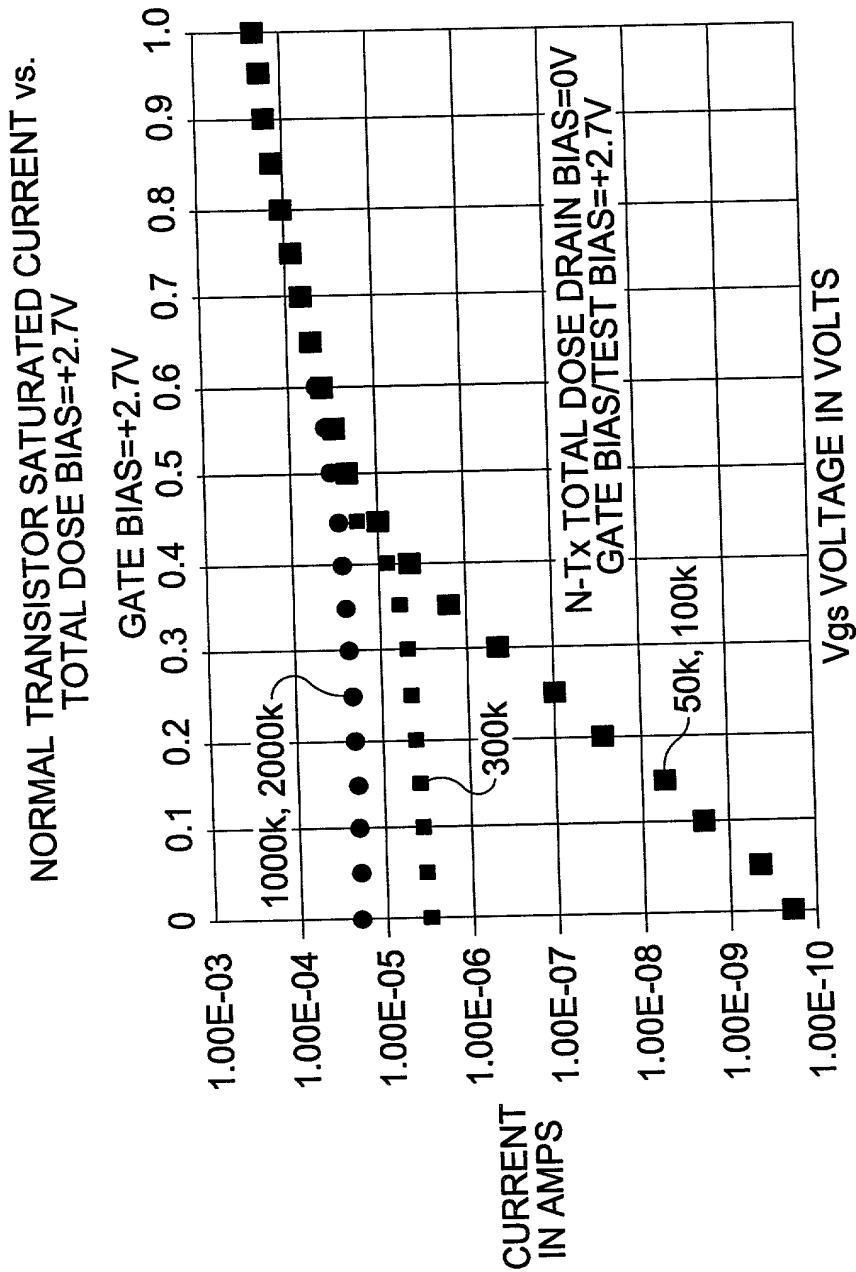


Fig. 5



- NORM 50K
- ▲ NORM 100K
- NORM 300K
- NORM 1000K
- NORM 2000K

Fig. 6

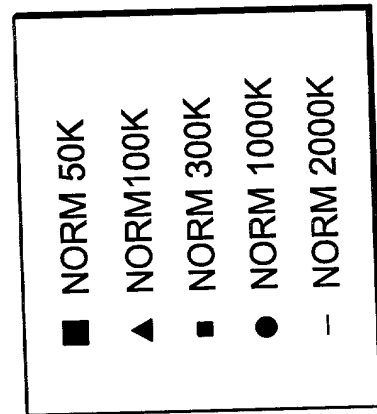
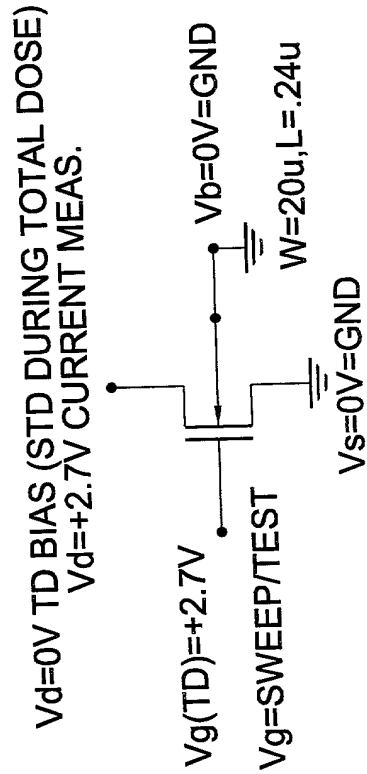
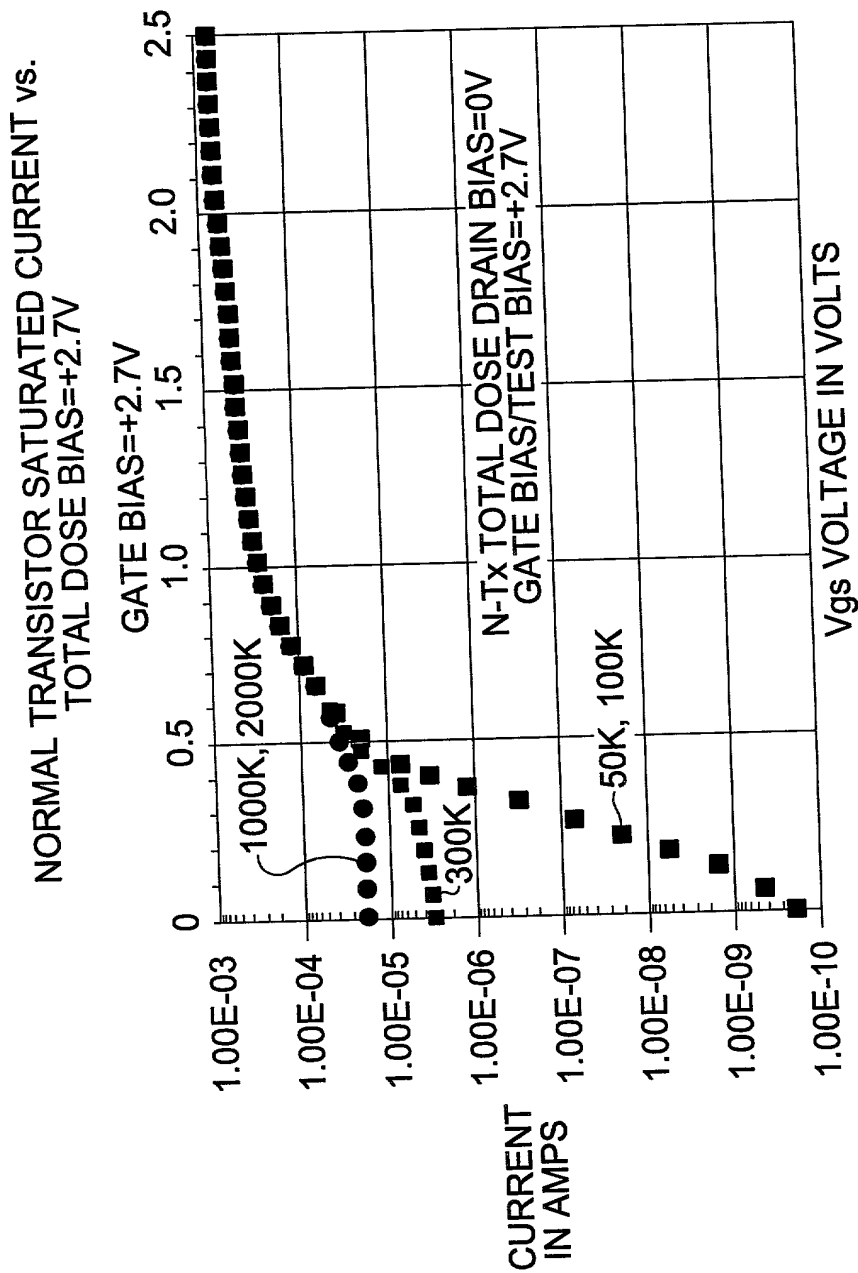


Fig. 7

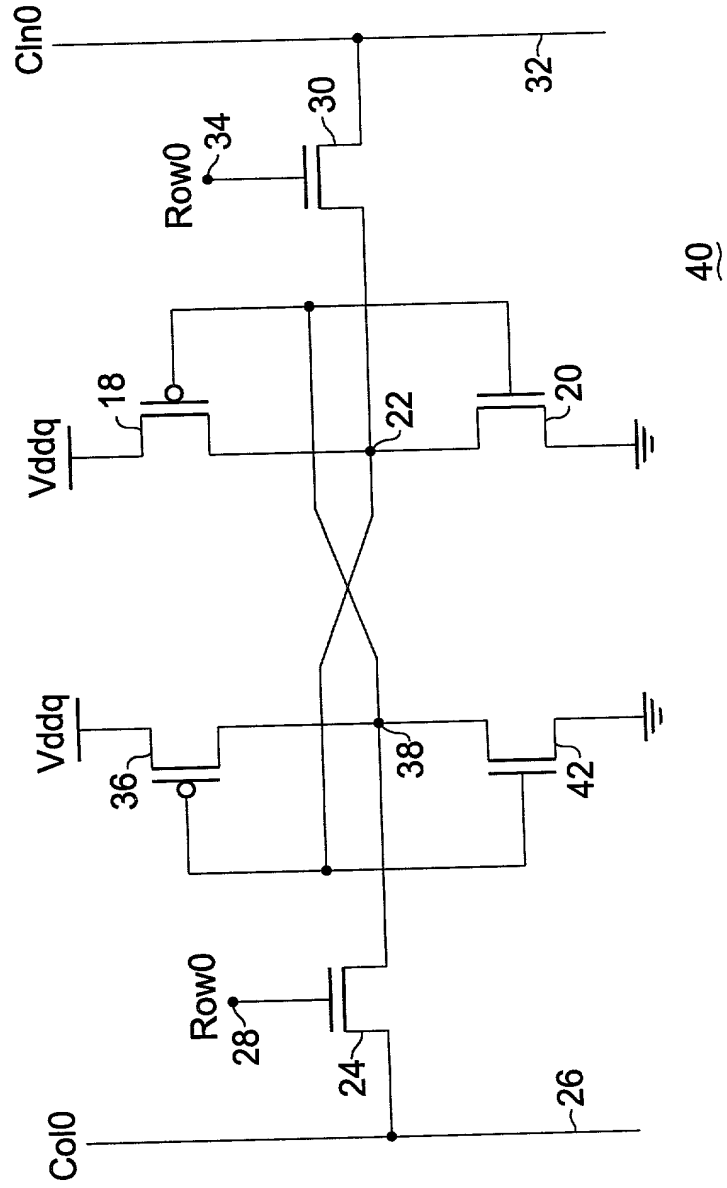
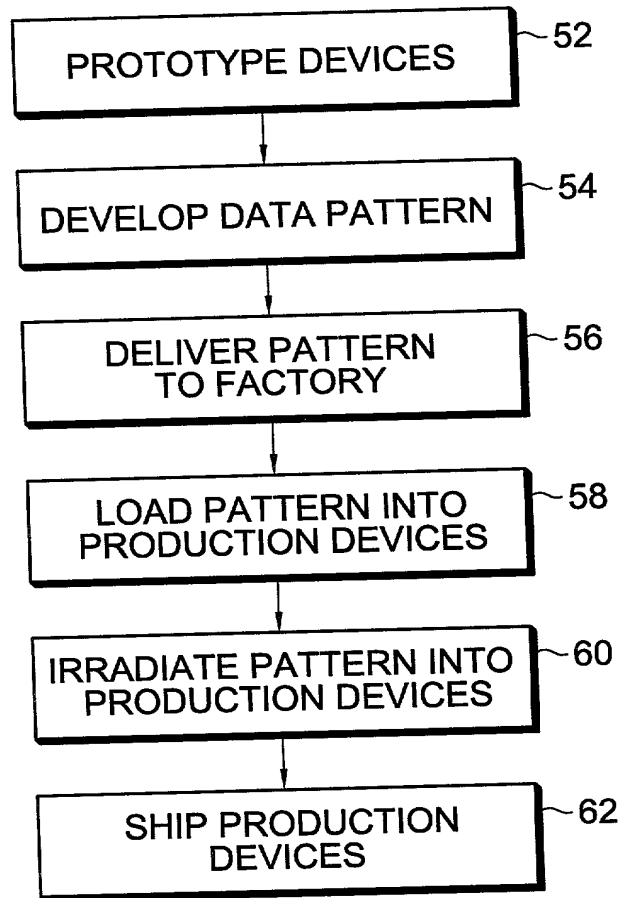
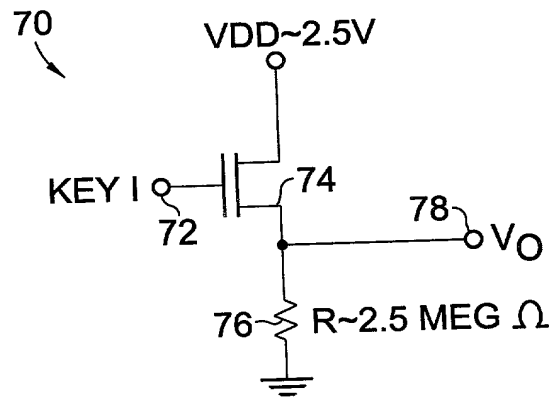
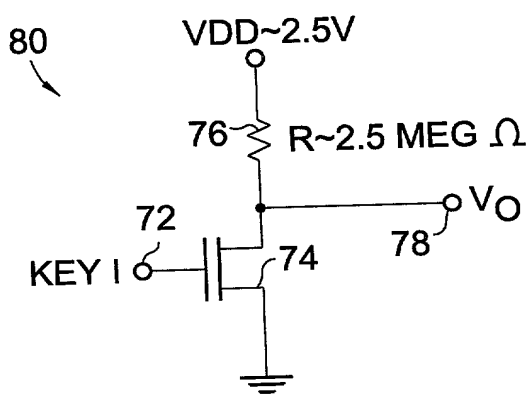
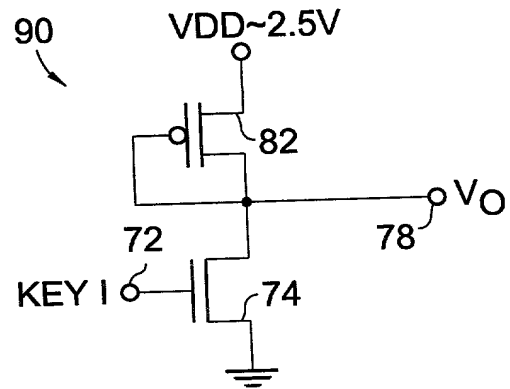
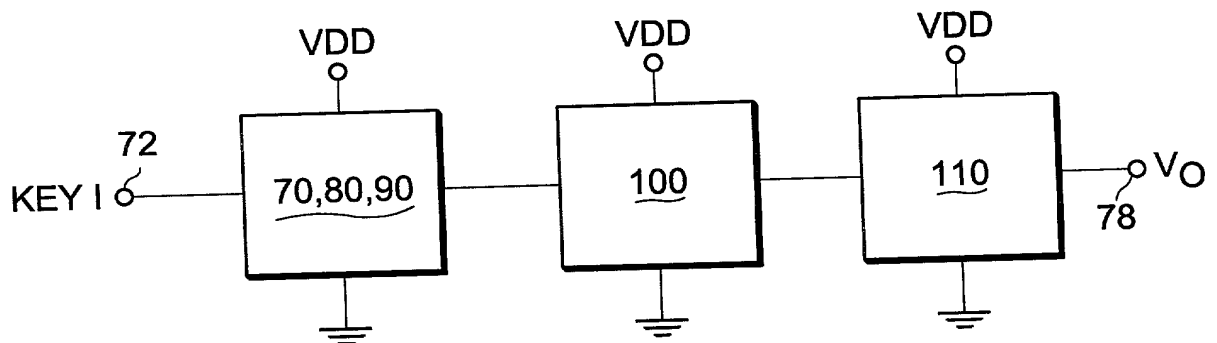


Fig. 8

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**Fig. 9**

**Fig. 10****Fig. 11****Fig. 12****Fig. 13**